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APPLICATION NO.	FILING I	DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/779,891	02/17/2	2004	Ronald B. Hulfachor	112055-0068U	9644	
24267	7590	06/14/2005		EXAM	EXAMINER	
CESARI AND MCKENNA, LLP 88 BLACK FALCON AVENUE BOSTON, MA 02210				WELLS, KE	ENNETH B	
				ART UNIT	PAPER NUMBER	
				2816	-	

DATE MAILED: 06/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
		10/779,891	HULFACHOR ET AL.	ano.			
	Office Action Summary	Examiner	Art Unit	6.10			
		Kenneth B. Wells	2816				
	The MAILING DATE of this communication a	ppears on the cover sheet with the	correspondence address				
THE I - Exter after - If the - If NO - Failur Any r	ORTENED STATUTORY PERIOD FOR REP MAILING DATE OF THIS COMMUNICATION resions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication, period for reply specified above is less than thirty (30) days, a reperiod for reply is specified above, the maximum statutory perior to reply within the set or extended period for reply will, by state eply received by the Office later than three months after the mained patent term adjustment. See 37 CFR 1.704(b).	I. 1.136(a). In no event, however, may a reply be to eply within the statutory minimum of thirty (30) dad will apply and will expire SIX (6) MONTHS fror to, cause the application to become ABANDON	imely filed lys will be considered timely. In the mailing date of this communication ED (35 U.S.C. § 133).	on.			
Status							
2a)	Responsive to communication(s) filed on 17 February 2004 . This action is FINAL . 2b) This action is non-final. Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispositi	on of Claims						
5)⊠ 6)⊠ 7)□	Claim(s) 1-10 is/are pending in the application 4a) Of the above claim(s) is/are withdred Claim(s) 9 and 10 is/are allowed. Claim(s) 1-10 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and	awn from consideration.					
Applicati	on Papers						
10)	The specification is objected to by the Examination The drawing(s) filed on is/are: a) and a complete an	ccepted or b) objected to by the e drawing(s) be held in abeyance. Section is required if the drawing(s) is of	ee 37 CFR 1.85(a). bjected to. See 37 CFR 1.121((d).			
Priority u	ınder 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some col None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.							
2) Notice 3) Inform	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/0 r No(s)/Mail Date 10/7/04.	4) Interview Summar Paper No(s)/Mail D 8) 5) Notice of Informal 6) Other:					

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1. Claims 1-10 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claims 1 and 9, it is incorrect to recite the VCO as including the control FET, receiving means and bipolar diode, i.e., these are the elements of the PLL, not the VCO (from applicant's figures, these elements are all upstream of the VCO 20, see instant Fig. 7 or Fig. 9). Also, on line 6 of claim 1, "the current" should be changed to --a portion of the drain current-- because this is what the figures show.

In claim 3, it is incorrect to recite "the diode" as including both the NPN and PNP base emitters (because this is actually two different diodes that include these two features, as shown in the instant figures).

In claim 4, it is incorrect to recite that the current through the second FET is in parallel with the current through the first FET, i.e., these currents are in series, not parallel.

Claim 6 is indefinite because it appears that the third N type MOSFET is actually part of the "means" already set forth in claim 1 (and thus should not be set forth as a further additional element of the invention).

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In claims 7 and 8, there is no antecedent basis for the recited fourth and fifth FETs (because no first through third FETs are set forth in claim 1).

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over Okamoto.

Note Fig. 8, where the recited "control FET" reads on either FET TR12 or FET TR14; and the recited "means for receiving" reads on either FET TR11 or TR13. Not disclosed is the recited "bipolar diode" (Okamoto only shows generic diodes D11 and D12), It would have been obvious, however, to one of ordinary skill in the art that generic diodes D11 and D12 can be made using either FETs or BJTs without any change in circuit operation of function, i.e., either type would work equally well in Okamoto's Fig. 8 circuitry. Thus, claim 1 does not distinguish patentably over Okamoto.

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3. Claims 9 and 10 are allowed.

Claims 2-8 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kenneth B. Wells whose telephone number is (571)272-1757. The examiner can normally be reached on Monday through Friday from 8:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan, can be reached at (571)272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on

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access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

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June 10, 2005